### EEPROM Data Structure

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Offset | Octet | 0 | | | | | | | | 1 | | | | | | | | 2 | | | | | | | | 3 | | | | | | | |
| Octet | Bit | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 |
| 0 | 0 | Gateway MAC Address | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 4 | 32 | *[reserved]* | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 8 | 64 | Node MAC Address | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 12 | 96 | *[reserved]* | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 16 | 128 | Node Name or Descriptor | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  |  |  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 64 | 512 | *[unassigned]* | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  |  |  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  |  |  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 128 | 1024 | Sequence Number | | | | | | | | | | | | | | | | *[reserved]* | | | | | | | | | | | | | | | |
| 132 | 1056 | Rain Counter | | | | | | | | | | | | | | | | *[reserved]* | | | | | | | | | | | | | | | |
| 136 | 1088 | Tank ID | | | | | | | | *[reserved]* | | | | | | | | Pump ID | | | | | | | | *[reserved]* | | | | | | | |
| 140 | 1200 | *[unassigned]* | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  |  |  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|  |  |  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

### Gateway MAC Address

|  |  |  |  |
| --- | --- | --- | --- |
| DC | xx | xx | xx |

Byte 0 = DC (Langlo devices; Other prefixes may be allocated as required)

Bytes 1–3 = Last three bytes of Gateway MAC address

Bytes 4–7 are reserved for future MAC address expansion

### Node MAC Address

|  |  |  |  |
| --- | --- | --- | --- |
| DC | xx | xx | xx |

Byte 8 = DC (Langlo devices; Other prefixes may be allocated as required)

Bytes 9–11 = Last three bytes of Node MAC address

Bytes 12–15 are reserved for future MAC address expansion

### Node Name or Descriptor

|  |
| --- |
|  |

Up to 48 bytes—Bytes 16 to 63—for an arbitrary byte string, perhaps a Node name or descriptor. This area is not ‘sacred’, it can be expanded or contracted if required.

### Sequence Number

|  |
| --- |
| n |

n = 0..65535

Bytes 128–129 = 16-bit counter. Sequence Number simply wraps after reaching 65535.

Bytes 130–131 are reserved for future expansion of the Sequence Number.

### Rain Counter

|  |
| --- |
| n |

n = 0..65535

Bytes 132–133 = 16-bit counter. The Rain Counter simply wraps after reaching 65535.

Bytes 134–135 are reserved for future expansion of the Rain Counter.

### Tank ID

|  |
| --- |
| n |

n = 0..255

Byte 136 = 8-bit identifier.

Byte 137 is reserved for future expansion of the Tank ID.

### Pump ID

|  |
| --- |
| n |

n = 0..255

Byte 136 = 8-bit identifier.

Byte 137 is reserved for future expansion of the Pump ID.